

U.S. PTO
10/087350
03/01/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10087350	FILING DATE 03/01/2002	CLASS 711.7A	SUBCLASS 102	GAU 2185	EXAMINER Follega
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APPLICANTS: McNeil Donald;

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2127

**CONTINUING DATA VERIFIED:
THIS APPLN CLAIMS BENEFIT OF 60/277,545 03/22/2001

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FOREIGN APPLICATIONS VERIFIED:

DO NOT PUBLISH <input type="checkbox"/>	SCIND <input type="checkbox"/>	ATTORNEY DOCKET NO 803-01
1 claim(s) only claimed 35 U.S.C. 119 conditions met	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	U.S. DEPT. OF COMM/PAT. & TM-PRO-40314 (Rev. 10-8-97)
Verifier/Examiner Acknowledged Examiner's initials		
TITLE : Ultra-modular processor in lattice topology		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
Amount Due	Date Paid	Total Claims	Print Claim for O.G
		DRAWING	
		Sheets Drawn	Figs. Drawn
		Print Fig.	
		Primary Examiner	
		Application Examiner	
ISSUE FEE		PREPARED FOR ISSUE	
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